

Electronic Patent Application Fee Transmittal

Application Number:	10728492			
Filing Date:	05-Dec-2003			
Title of Invention:	Low power memory controller with leaded double data rate DRAM package arranged on a two layer printed circuit board			
First Named Inventor/Applicant Name:	Eric Hung			
Filer:	Kevin L. Daffer/Pamela Gerik			
Attorney Docket Number:	03-1237 (5201-27600)			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Submission- Information Disclosure Stmt	1806	1	180	180
Total in USD (\$)				180